

FIG. 3

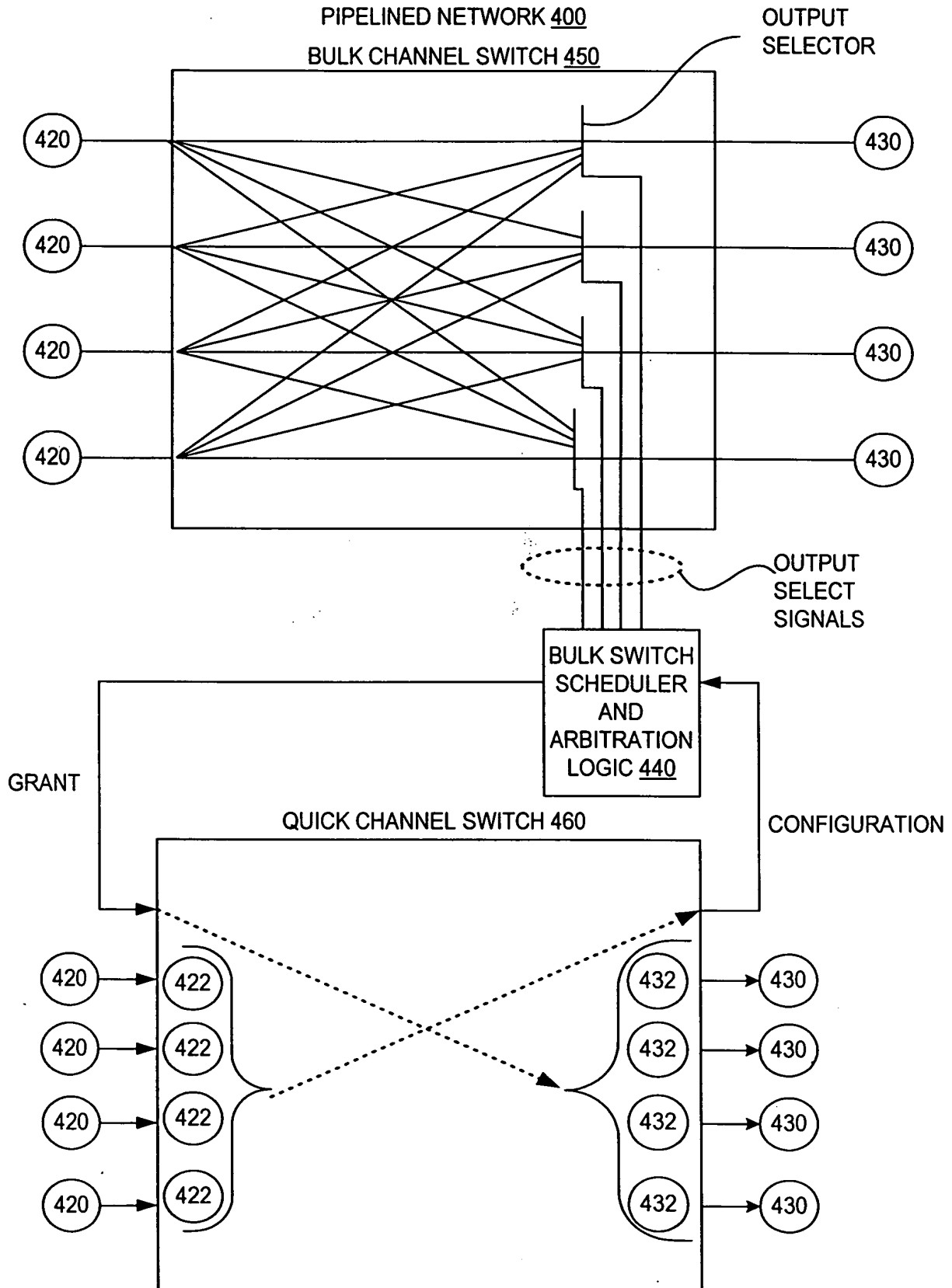


FIG. 4

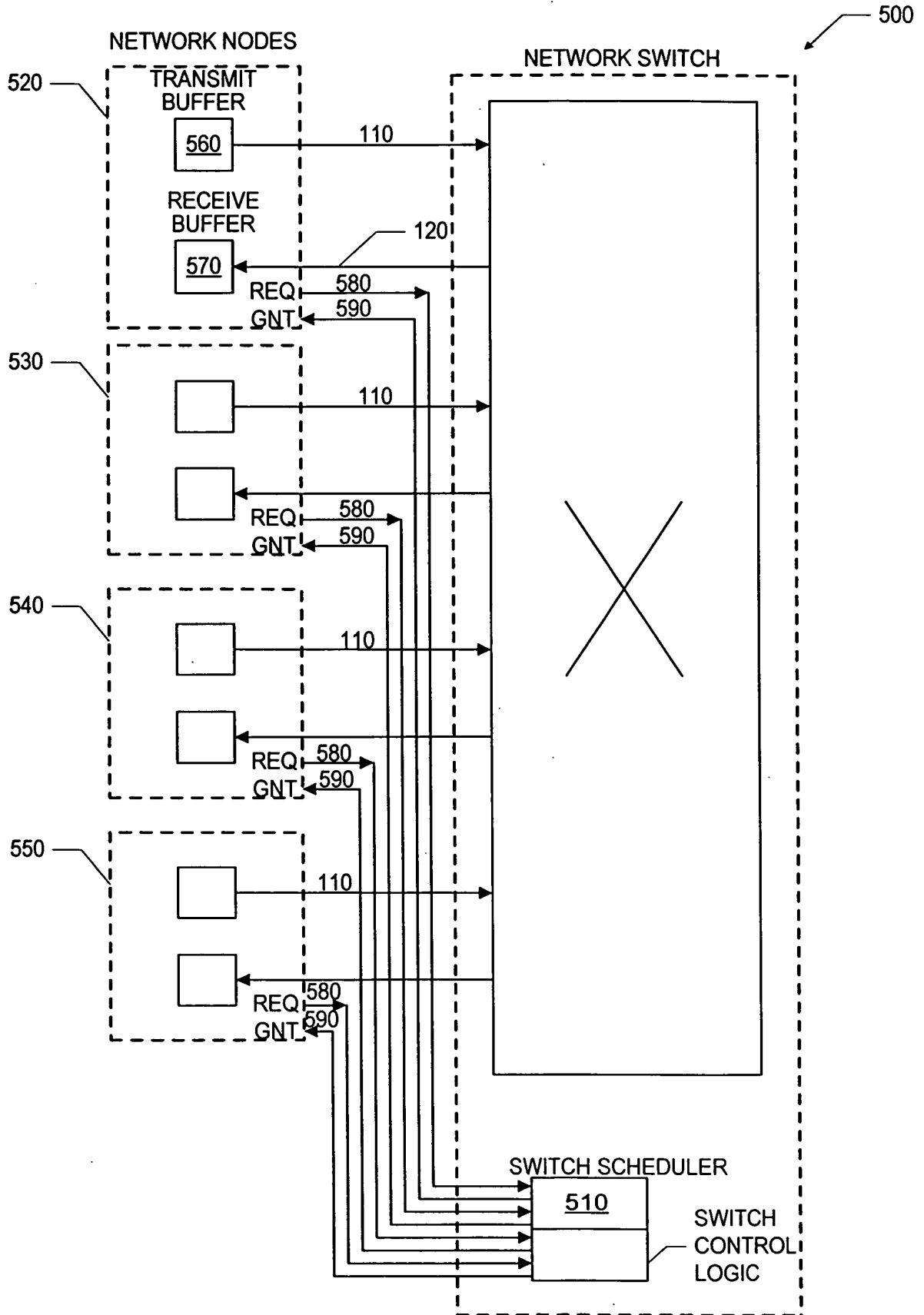


FIG. 5

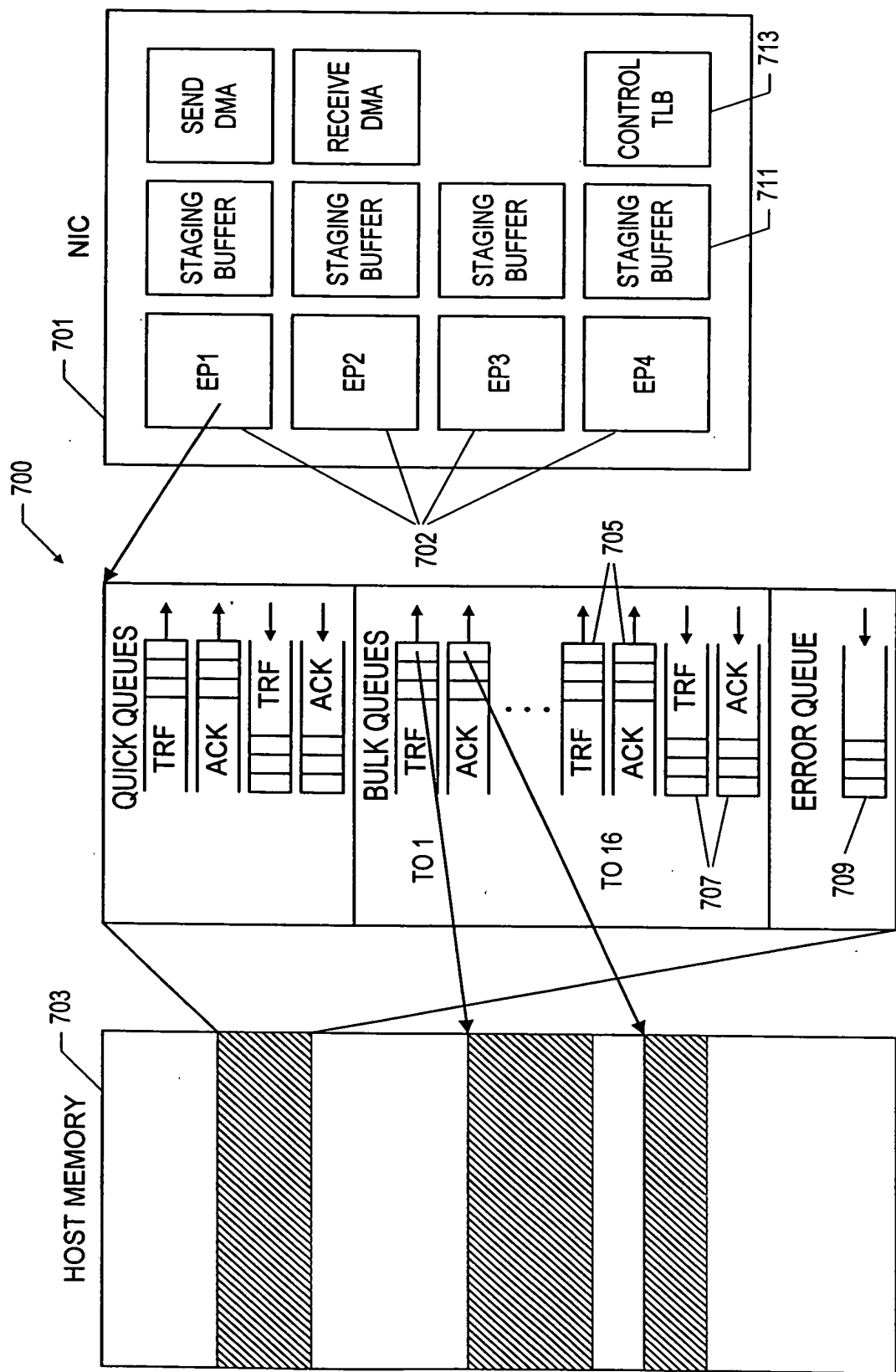


FIG. 7

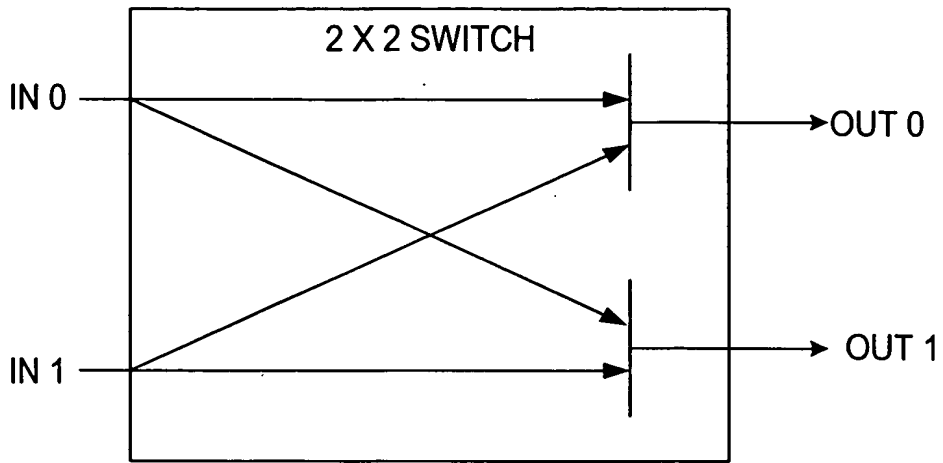


FIG. 8

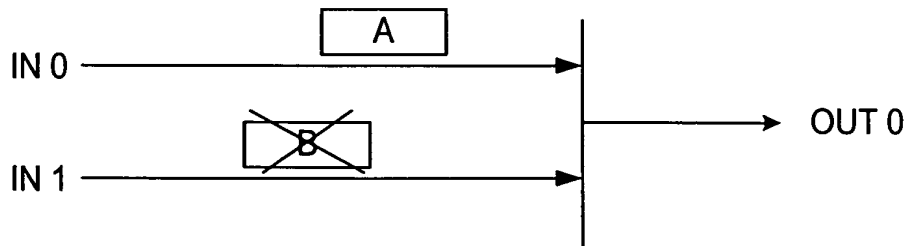


FIG. 9A

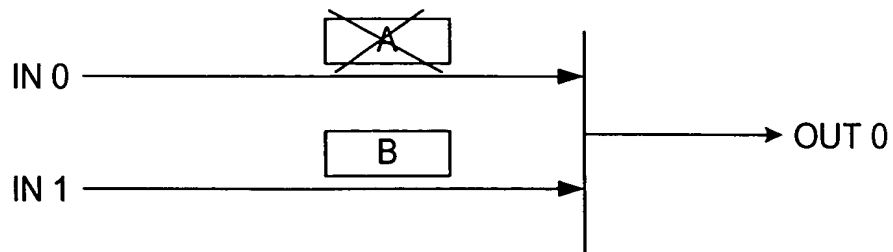


FIG. 9B

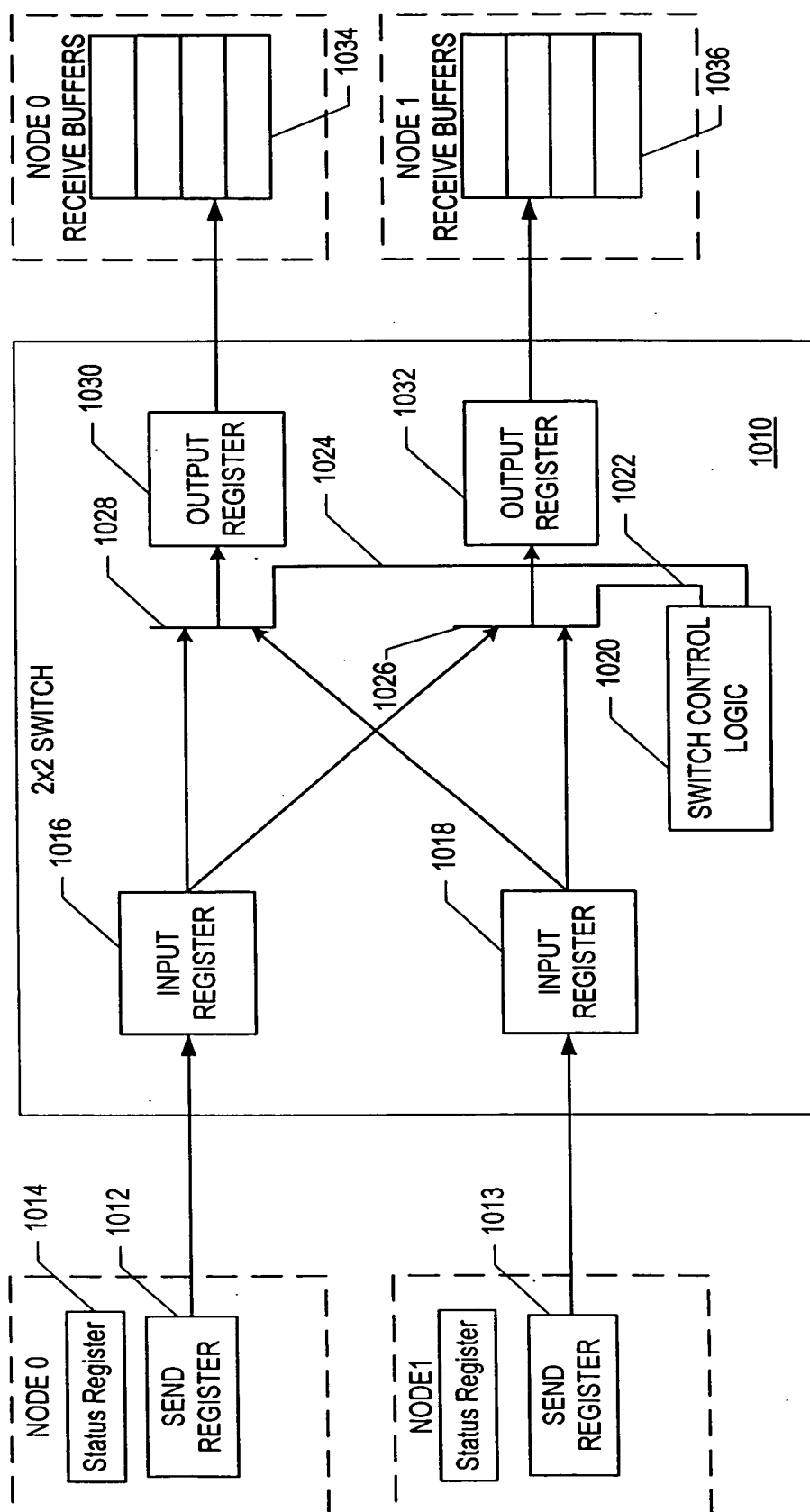


FIG. 10

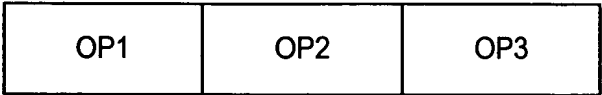


FIG. 11A

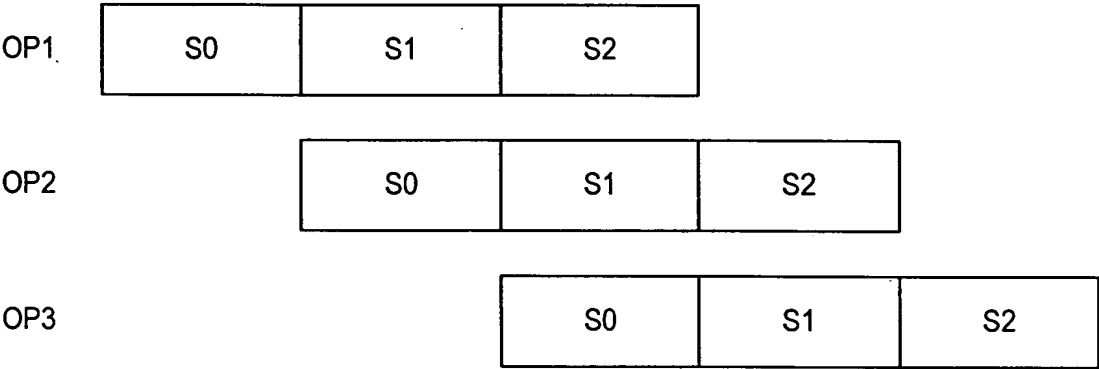


FIG. 11B

00TEED" OE404560

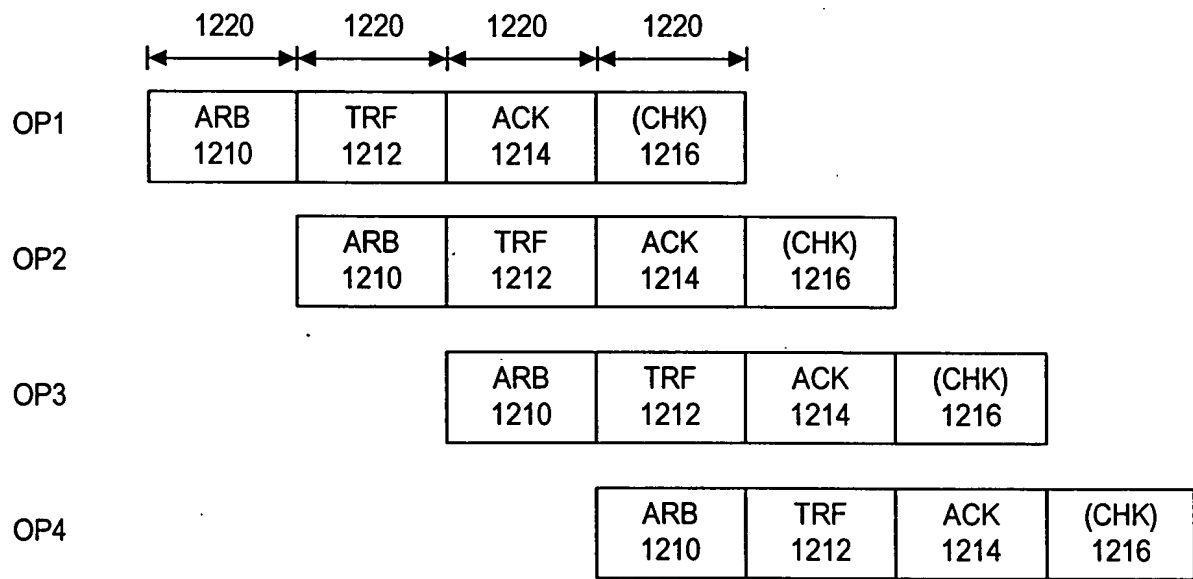


FIG. 12

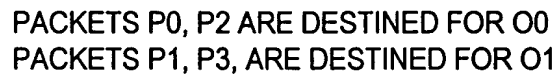


Diagram illustrating a 4x4 crossbar switch configuration. The switch has four input ports (I0, I1) and four output ports (P0, P1, P2, P3). Each port has a 4-bit data path. The connections are:

- I0 to P0: 1402
- I1 to P1: 1404
- I1 to P2: 1401 and 1403
- I1 to P3: 1403

The data paths are labeled with ARB, TRF, ACK, and CHK.

FIG. 15 is a diagram illustrating a sequence of operations or data frames. The diagram shows a series of frames, each containing four fields: ARB, TRF, ACK, and CHK. The frames are arranged in a staggered, overlapping manner, suggesting a sequence of operations or data transmission. The frames are labeled as follows:

- Frame 1502: P0 (ARB, TRF, ACK, CHK)
- Frame 1504: P1 (ARB, TRF, ACK, CHK)
- Frame 1501: P2 (ARB, TRF, ACK, CHK)
- Frame 1503: P2 (ARB, TRF, ACK, CHK)
- Frame 1507: P3 (ARB, TRF, ACK, CHK)
- Frame 1505: P3 (ARB, TRF, ACK, CHK)
- Frame 1509: P2' (ARB, TRF, ACK, CHK)
- Frame 1511: P3' (ARB, TRF, ACK, CHK)

The diagram shows a sequence of operations or data frames, with each frame containing four fields: ARB, TRF, ACK, and CHK. The frames are arranged in a staggered, overlapping manner, suggesting a sequence of operations or data transmission. The frames are labeled as follows:

- Frame 1502: P0 (ARB, TRF, ACK, CHK)
- Frame 1504: P1 (ARB, TRF, ACK, CHK)
- Frame 1501: P2 (ARB, TRF, ACK, CHK)
- Frame 1503: P2 (ARB, TRF, ACK, CHK)
- Frame 1507: P3 (ARB, TRF, ACK, CHK)
- Frame 1505: P3 (ARB, TRF, ACK, CHK)
- Frame 1509: P2' (ARB, TRF, ACK, CHK)
- Frame 1511: P3' (ARB, TRF, ACK, CHK)

